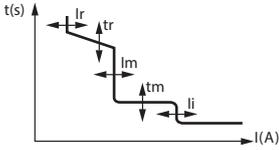


Settings of the microprocessor protection units

MP4 LSI

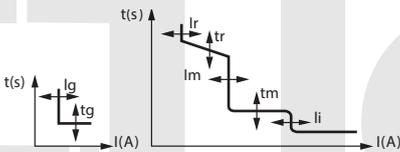
I_r , t_r , I_m , t_m , I_i adjustment on front panel



- **Long time delay protection against overloads**
 I_r from 0.4 to $1 \times I_n$ (6 + 6 steps) on two selectors (0.4 ÷ 0.9, by steps of 0.1 and 0.0 ÷ 0.1, by steps of 0.02)
- **Long delay protection operation time**
 t_r - at $6 \times I_r$ (4 + 4 steps) $t_r = 5-10-20-30$ s (MEM ON) 30-20-10-5 s (MEM OFF)
- **Short time delay protection against short circuits**
 I_m from 1.5 to $10 \times I_r$ (9 steps) $I_m = 1.5-2-2.5-3-4-5-6-8-10 \times I_r$
- **Short time delay protection operation time**
 t_m from 0 to 0.3 s (4 + 4 steps) $t_m = 0-0.1-0.2-0.3$ s ($t = \text{cost}$), 0.3-0.2-0.1-0.01 s ($I^2t = \text{constant}$)
- **Instantaneous protection against very high short circuits**
 I_i from 2 to $15 \times I_n$ or I_{cw} (9 steps) $I_i = \text{off}-2-3-4-6-8-10-12-15 \times I_n$ or I_{cw}
- **Neutral protection:** IN = I-II-III-IV x I_r (0-50-100-100 %)

MP4 LSIg

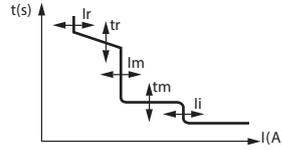
I_r , t_r , I_i , I_g , t_g , I_m , t_m , adjustment on front panel



- **Long time delay protection against overloads**
 I_r from 0.4 to $1 \times I_n$ (6 + 6 steps) on two selectors (0.4 ÷ 0.9, by steps of 0.1 and 0.0 ÷ 0.1, by steps of 0.02)
- **Long delay protection operation time**
 t_r - at $6 \times I_r$ (4 + 4 steps) $t_r = 5-10-20-30$ s (MEM ON) 30-20-10-5 s (MEM OFF)
- **Short time delay protection against short circuits**
 I_m from 1.5 to $10 \times I_r$ (9 steps) $I_m = 1.5-2-2.5-3-4-5-6-8-10 \times I_r$
- **Short time delay protection operation time**
 t_m from 0 to 0.3 s (4 + 4 steps) $t_m = 0-0.1-0.2-0.3$ s ($t = \text{constant}$), 0.3-0.2-0.1-0.01 s ($I^2t = \text{constant}$)
- **Instantaneous protection against very high short circuits**
 I_i from 2 to $15 \times I_n$ or I_{cw} (9 steps) $I_i = \text{OFF}-2-3-4-6-8-10-12-15 \times I_n$ or I_{cw}
- **Earth fault current**
 I_g from 0.2 to $1 \times I_n$ (9 steps) $I_g = 0.2-0.3-0.4-0.5-0.6-0.7-0.8-1 \times I_n$, OFF
- **Time delay on earth fault tripping**
 t_g from 0.1 to $1 \times I_n$ (4 steps) $T_g = 0,1-0,2-0,5-1$ s (both $t = \text{constant}$ and $I^2t = \text{constant}$)
- **Neutral protection:** IN = I-II-III-IV x I_r (0-50-100-100 %)

MP6 LSI

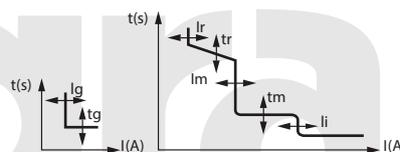
I_r , t_r , I_m , t_m , I_i adjustment on front panel



- **Long time delay protection against overloads**
 I_r from 0.4 to $1 \times I_n$ (7 steps) $I_r = 0.4-0.5-0.6-0.7-0.8-0.9-1 \times I_n$
- **Long delay protection operation time**
 t_r - at $6 \times I_r$ (4 steps) $t_r = 5-10-20-30$ s (both MEM ON and MEM OFF)
- **Short time delay protection against short circuits**
 I_m from 1.5 to $10 \times I_r$ (9 steps) $I_m = 1.5-2-2.5-3-4-5-6-8-10 \times I_r$
- **Short time delay protection operation time**
 t_m from 0.03 to 1 s (11 steps) $t_m = 0.03-0.1-0.2-0.3-0.4-0.5-0.6-0.7-0.8-0.9-1$ s (both $t = \text{constant}$ and $I^2t = \text{constant}$)
- **Instantaneous protection against very high short circuits**
 I_i from 2 to $15 \times I_n$ or I_{cw} (9 steps) $I_i = 2-3-4-6-8-10-12-15 \times I_n$ or I_{cw}
- **Neutral protection:** IN = I-II-III-IV x I_r (0-50-100-100 %)

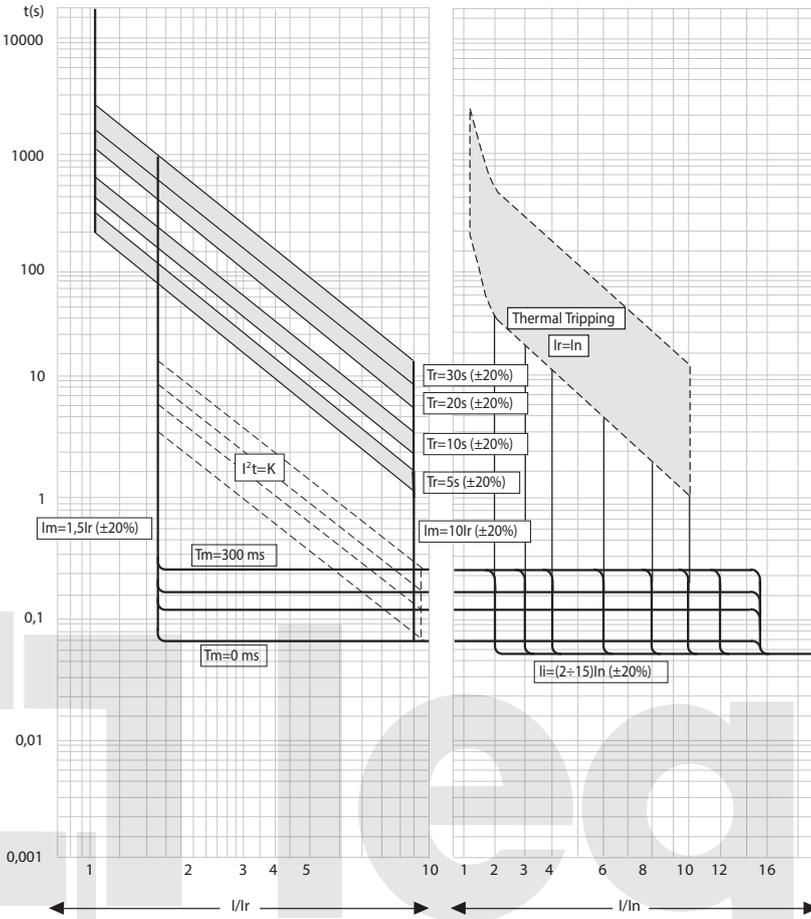
MP6 LSIg

I_r , t_r , I_i , I_g , t_g , I_m , t_m , adjustment on front panel



- **Long time delay protection against overloads**
 I_r from 0.4 to $1 \times I_n$ (7 steps) $I_r = 0.4-0.5-0.6-0.7-0.8-0.9-1 \times I_n$
- **Long delay protection operation time**
 t_r - at $6 \times I_r$ (4 steps) $t_r = 5-10-20-30$ s (both MEM ON and MEM OFF)
- **Short time delay protection against short circuits**
 I_m from 1.5 to $10 \times I_r$ (9 steps) $I_m = 1.5-2-2.5-3-4-5-6-8-10 \times I_r$
- **Short time delay protection operation time**
 t_m from 0.03 to 1 s (11 steps) $t_m = 0.03-0.1-0.2-0.3-0.4-0.5-0.6-0.7-0.8-0.9-1$ s (both $t = \text{constant}$ and $I^2t = \text{constant}$)
- **Instantaneous protection against very high short circuits**
 I_i from 2 to $15 \times I_n$ or I_{cw} (9 steps) $I_i = 2-3-4-6-8-10-12-15 \times I_n$ or I_{cw}
- **Earth fault current**
 I_g from 0.2 to $1 \times I_n$ (9 steps) $I_g = 0.2-0.3-0.4-0.5-0.6-0.7-0.8-1 \times I_n$, OFF
- **Time delay on earth fault tripping**
 t_g from 0.1 to $1 \times I_n$ (4 steps) $T_g = 0,1-0,2-0,5-1$ s (both $t = \text{constant}$ and $I^2t = \text{constant}$)
- **Neutral protection:** IN = I-II-III-IV x I_r (0-50-100-100 %)

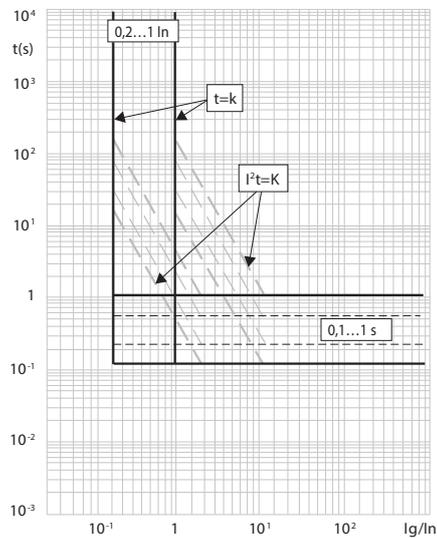
Selective time-current tripping characteristic for MP4 protection units



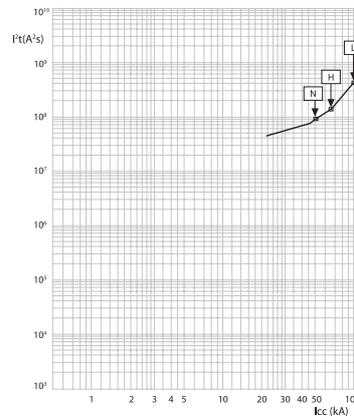
If short-circuit current is higher than I_{cw} value or I_i is setted at I_{cw} position, tripping time is equal to 30ms

- I_r = long time setting current
- T_r = long time delay
- I_m = short time setting current
- T_m = short time delay
- I_f = instantaneous intervention current

Ground fault tripping curve for MP4 LSIG protection unit



Let through energy characteristics



I_{cc} (kA) = estimated short circuit symmetrical current (RMS value)
 I^2t (A²s) = pass-through specific energy